



# WHM0530AE

## 0.5- 3.0 GHz LOW NOISE WIDE BAND AMPLIFIER

REV E  
January, 2015

### Key Features



- 50 Ohm Impedance
- 0.5 ~ 3.0 GHz
- 1.0 dB Noise Figure
- 20.0 dBm Min Output IP<sub>3</sub>
- 15.5 dB Gain
- +/-0.5 dB Gain Flatness
- 10.0 ~ 14.0 dBm P<sub>1dB</sub>
- 1.35:1 VSWR Fully Matched
- Single Power Supply
- >300 Years MTBF
- RoHS Compliance
- MSL-1 Moisture Sensitivity Level

### Product Description

WHM0530AE is integrated with WanTcom proprietary low noise amplifier technologies, high frequency micro electronic assembly techniques, and high reliability designs to achieve optimum low noise figure, wideband, and high performances together. With single +5.0V DC operation, the amplifier has optimal input and output matching in the specified frequency range at 50-Ohm impedance system. The amplifier has standard 0.25" x 0.25" x 0.065" surface mount package.

The amplifier is designed to meet the rugged standard of MIL-STD-883g.

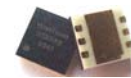
CAUTION:



ELECTROSTATIC DISCHARGE SENSITIVE

### Applications

- Mobile Infrastructures
- GPS
- WiMAX
- Defense
- Security System
- Measurement
- Fixed Wireless
- Avionics



### Specifications

Summary of the key electrical specifications at room temperature

Index	Testing Item	Symbol	Test Constraints	Min	Nom	Max	Unit
1	Gain	S <sub>21</sub>	0.5 – 3.0 GHz	14.5	15.5	16.5	dB
2	Gain Variation	ΔG	0.5 – 3.0 GHz		+/-0.5	+/-1.0	dB
3	Input Return Loss	S <sub>11</sub>	0.5 – 3.0 GHz	14	16		dB
4	Output Return Loss	S <sub>22</sub>	0.5 – 3.0 GHz	14	16		dB
5	Reverse Isolation	S <sub>12</sub>	0.5 – 3.0 GHz	16			dB
6	Noise Figure	NF	0.5 – 3.0 GHz		1.0	1.2	dB
7	Output Power 1dB Compression Point	P <sub>1dB</sub>	0.5 – 3.0 GHz	9		15	dBm
8	Output-Third-Order Interception Point	IP <sub>3</sub>	Two-Tone, P <sub>out</sub> = 0 dBm each, 1 MHz separation	20			dBm
9	Current Consumption	I <sub>dd</sub>	V <sub>dd</sub> = +5.0 V		20		mA
10	Power Supply Operating Voltage	V <sub>dd</sub>		+4.7	+5	+5.3	V
11	Thermal Resistance	R <sub>th,c</sub>	Junction to case			215	°C/W
12	Operating Temperature Range	T <sub>o</sub>		-40		+85	°C
13	Maximum Input CW RF Power	P <sub>IN, MAX</sub>	DC – 6.0 GHz			10	dBm

### Absolute Maximum Ratings

Parameters	Units	Ratings
DC Power Supply Voltage	V	-0.5 ~ +7.0
Drain Current	mA	40
Total Power Dissipation	mW	280
Input CW RF Power	dBm	10
Channel Temperature	°C	150
Storage Temperature	°C	-65 ~ 150
Operating Temperature	°C	-55 ~ +100
Thermal Resistance	°C/W	215

Operation of this device beyond any one of these parameters may cause permanent damage.

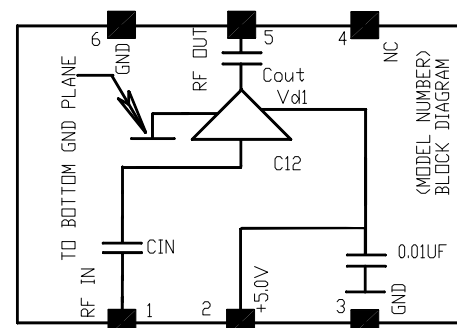
### Ordering Information

Model Number	WHM0530AE
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ESD tube with the capacity of 25 pieces is used for the packing. Contact factory for tape and reel packing option for higher volume order.

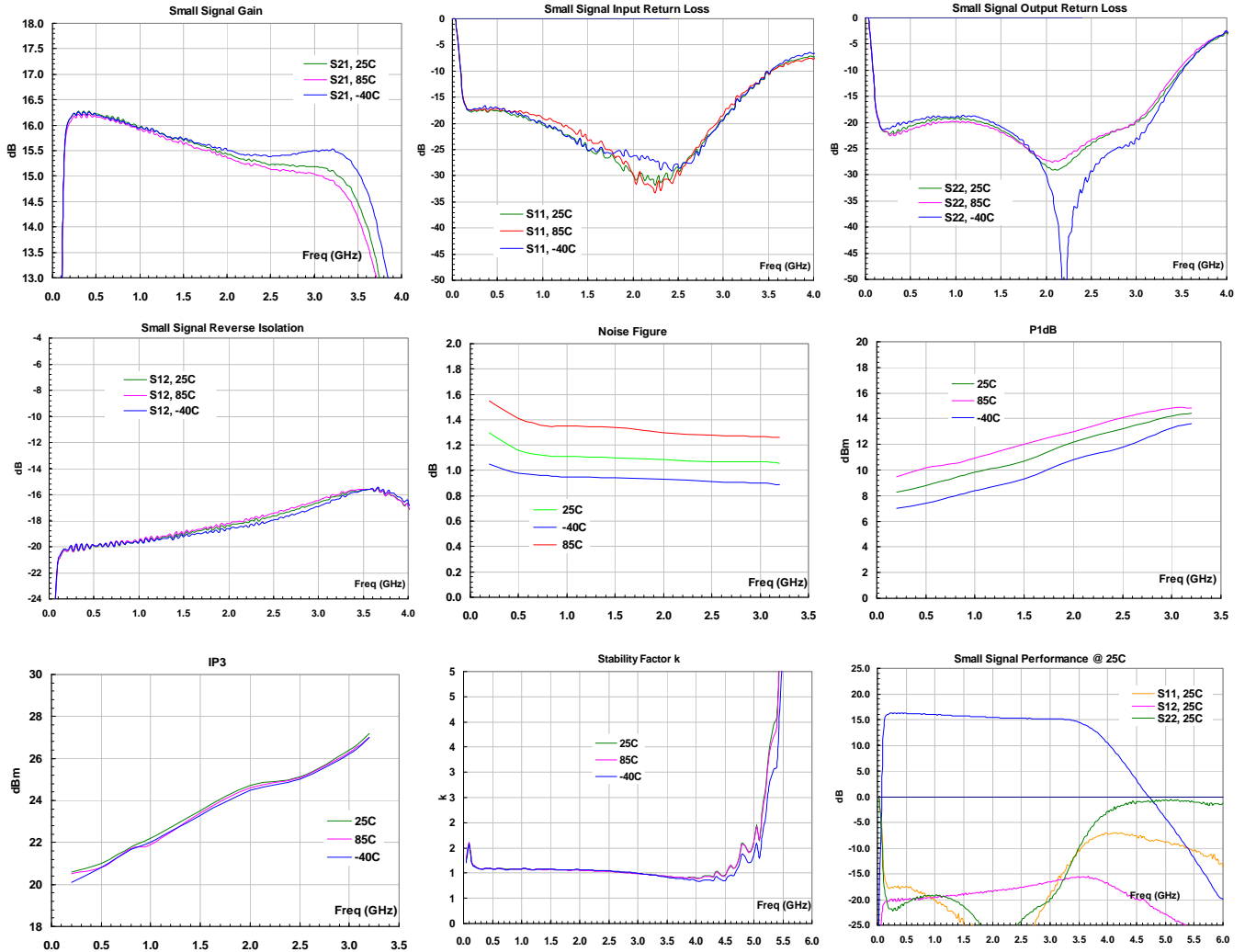
Specifications and information are subject to change without notice.

### Functional Block Diagram

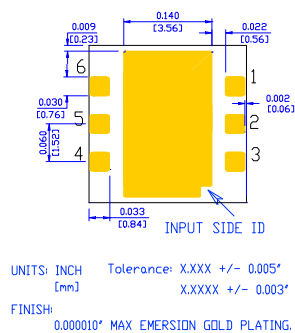
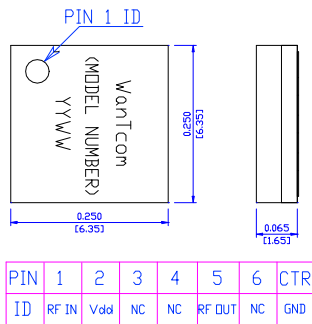




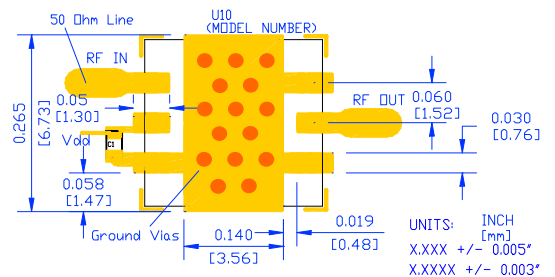
**Typical Data**



**Outline, WHM-1S**



**Foot Print/Motherboard Layout**



- NOTE:
1. THE BACKSIDE NEEDS TO BE METAL GROUND LAYER
  2. GROUND VIA DIAMETER IS 0.024" (0.61 mm)
  3. C1 IS 0.01 UF OR LARGER VALUE CAPACITOR
  4. MATERIAL: FR-4, 4000-13, FROM NELCO
  5. USE PROPER WIDTH FOR 50-OHM LINES FOR OTHER PCB MATERIAL

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# WHM0530AE

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### Small Signal S-Parameters:

! WHM0530AE, +25C, De-embedded S-parameters at  $V_{dd}=+5V$ ,  $I_{dd}=20mA$ , LRM calibration, last updated 10/1/07.  
# GHz s MA R 50

!Freq(GHz)	MAGS11	ANGS11	MAGS21	ANGS21	MAGS12	ANGS12	MAGS22	ANGS22
0.05	0.777	-114.1	1.958	-64	0.031	-141.1	0.742	-117.1
0.1	0.27	145.3	3.453	-97.5	0.087	104.9	0.215	160.1
0.2	0.132	55.2	6.089	-149.9	0.096	40.7	0.083	107.3
0.3	0.137	16.4	6.407	178.9	0.101	18.1	0.082	74.2
0.4	0.13	-4.3	6.418	158.1	0.098	4	0.084	54.2
0.5	0.133	-19.8	6.42	141.2	0.101	-6.2	0.092	41.6
0.6	0.122	-34.4	6.386	125.6	0.1	-16.6	0.098	29.4
0.7	0.122	-46	6.36	111.8	0.103	-23.4	0.106	20.5
0.8	0.116	-56.9	6.313	98.3	0.103	-31.3	0.109	10.7
0.9	0.112	-68.2	6.258	85.4	0.103	-36.9	0.111	2.5
1	0.101	-75.6	6.216	72.8	0.104	-45	0.111	-7.6
1.1	0.093	-85.3	6.175	60.5	0.106	-51.1	0.11	-16.3
1.2	0.089	-91.9	6.158	48.1	0.109	-58.4	0.108	-25.6
1.3	0.079	-101.1	6.097	36.3	0.109	-63.4	0.102	-33.1
1.4	0.074	-108	6.061	24.3	0.111	-69.8	0.095	-41.8
1.5	0.061	-115.1	6.026	12.2	0.111	-76.6	0.086	-48.9
1.6	0.06	-121.4	6.001	0.4	0.114	-82.9	0.078	-56.6
1.7	0.055	-123.7	5.951	-11.4	0.115	-89.3	0.066	-62.6
1.8	0.051	-126.9	5.944	-23.1	0.118	-95.1	0.055	-67
1.9	0.046	-130.9	5.894	-35	0.118	-101.9	0.043	-67
2	0.04	-139.7	5.865	-47.2	0.12	-108.4	0.034	-63.3
2.1	0.041	-142	5.833	-59.2	0.122	-115.5	0.028	-49.7
2.2	0.036	-153	5.799	-71.2	0.125	-121.4	0.028	-33.3
2.3	0.034	-162.1	5.798	-83.3	0.127	-128.3	0.035	-23.3
2.4	0.034	-173	5.769	-95.3	0.128	-134.6	0.044	-25.1
2.5	0.047	179.1	5.764	-107.8	0.131	-142.2	0.052	-28.9
2.6	0.049	160	5.758	-120.1	0.134	-149.3	0.061	-40
2.7	0.069	146.6	5.757	-132.6	0.138	-156.5	0.068	-53.3
2.8	0.072	128.9	5.744	-145.7	0.141	-163.8	0.076	-72.2
2.9	0.093	117.8	5.741	-159	0.144	-171.1	0.081	-93
3	0.109	96.8	5.728	-172.7	0.147	-179.5	0.095	-117.8
3.1	0.143	84.5	5.703	173.2	0.152	172.8	0.11	-144.7
3.2	0.174	67.9	5.665	158.7	0.156	164.1	0.141	-170.5
3.3	0.215	53.1	5.571	143.4	0.159	154.8	0.182	164.5
3.4	0.244	36.3	5.444	127.4	0.163	145.1	0.245	142.3
3.5	0.289	18.5	5.243	110.7	0.164	134.7	0.315	120.7
3.6	0.336	0.6	4.997	93.7	0.165	124.4	0.399	101.6
3.7	0.375	-16.6	4.628	76.6	0.162	112.9	0.483	81.4
3.8	0.397	-33.2	4.217	59.5	0.158	102.6	0.571	63.8
3.9	0.418	-51.3	3.74	42.7	0.151	90.9	0.64	44.8
4	0.426	-67.6	3.274	26.2	0.144	81.3	0.719	30.3
4.1	0.437	-82.8	2.834	10.4	0.134	70.9	0.773	13.8
4.2	0.436	-97.8	2.426	-4.2	0.125	63.1	0.803	1.7
4.3	0.428	-110.6	2.07	-18.3	0.116	53	0.848	-13.5
4.4	0.419	-123.7	1.75	-31.6	0.107	46.3	0.862	-24.7
4.5	0.413	-134.1	1.475	-44.6	0.102	38.5	0.901	-38.8
5	0.363	176.5	0.672	-97.3	0.073	-1.6	0.926	-85.5
5.5	0.302	142.6	0.304	-141.2	0.047	-43.7	0.881	-127.8
6	0.211	115.1	0.14	-163.3	0.033	-68.8	0.869	-159.6

Specifications and information are subject to change without notice.



### Application Notes:

#### A. Motherboard Layout

The recommended motherboard layout is shown in **Figure 1**. Sufficient numbers of ground vias on center ground pad are essential for the RF grounding. The width of the 50-Ohm microstrip lines at the input and output RF ports may be different property of the substrate. The ground plane on the backside of the substrate is needed to connect the center ground pad through the vias. The ground plane is also essential for the 50-Ohm microstrip line launches at the input and output ports.

The +5V DC voltage is applied at Pin 2. For +5V line trace length being longer than 6 inches without a decoupling capacitor, a 0.1 uF de-coupling capacitor,  $C_1$ , with minimum rating voltage of 10V is needed across the +5V pin to ground. The capacitor must be rated in the temperature range of  $-55^{\circ}\text{C}$  to  $100^{\circ}\text{C}$  to ensure the entire circuit work in the specified temperature range.  $C_1$  needs to locate very close to Pin 2 to eliminate the possible RF input signal loss due to the coupling between Pin 1 and Pin 2. Failing to do that may cause 0.10 dB to 0.20 dB additional loss at the input port and thus the noise figure will increase at that amount.

No DC block capacitor is required at input and output RF ports. The NC pins connected to ground are recommended.

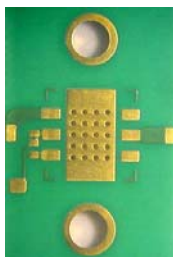


Fig. 1 Motherboard foot print

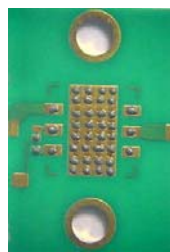


Fig. 2 Dispensed solder paste

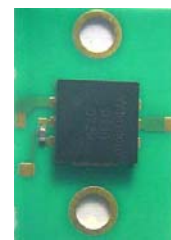


Fig. 3 Assembled part

#### B. Assembly

The regular low temperature and none clean solder paste such as SN63/Pb37 is recommended. The high temperature solder has been used internally for the WHM series amplifier assembly. The melting temperature point of the high temperature solder is around  $240^{\circ}\text{C}$ . Thus, melting temperature of the solder paste should be below  $220^{\circ}\text{C}$  for assembling WHM series amplifier on the test board to reduce the possible damage. The temperature melting point of the SN63/Pb37 solder paste is around  $183^{\circ}\text{C}$  and is suitable for the assembly purpose.

The SN63/Pb37 solder paste can be dispensed by a needle manually or driven by a compressed air. **Figure 2** shows the example of the dispensed solder paste pattern. Each solder paste dot is in the diameter of  $0.005'' \sim 0.010''$  ( $0.125 \sim 0.250$  mm).

For volume assembly, a stencil with  $0.004''$  ( $0.10$  mm) is recommended to print the solder paste on the circuit board.

For more detail assembly process, refer to AN-109 at [www.wantcominc.com](http://www.wantcominc.com) website.

#### C. Electrical Testing and Fine Tuning

The amplifier is designed to be fully matched at the input and output ports. Any tuning is not needed. However, when connecting the assembled amplifier to a device such as a SMA connector or a filter, the connecting point or joint point could affect mainly the return loss at the port due to the non-ideal 50-Ohm impedance of the device. By varying the connection feature size such as the solder amount to get the optimum return loss or best matching result at the interface. This fine-tuning has little affect on the other performance such as gain, noise figure,  $P_{1dB}$ , or  $IP_3$ .

During the fine-tuning process, a vector network analyzer can be used to monitoring the return loss at the port while varying the feature size of the joint point. Varying the connection feature size until the optimum return loss is achieved.

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